IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

and

Application of

Peter STAUSS et al.

Serial No.:

10/544,159

Filed:

For:

Thin-Film Semiconductor Component

Production Method for said Component

Examiner: WEISS, Howard

Group Art: 2814

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

March 13, 2008 (Date of Deposit)

Thomas Langer

Signature

March 13, 2008 Date of Signature

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INFORMATION DISCLOSURE STATEMENT

SIR:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the

It is respectfully requested that the above information be considered by the Examiner and that the copy of the enclosed form PTO/SB/08A be returned indicating that such information has been considered.

In accordance with 37 C.F.R §§1.97(g) and (h), the filing of this Information Disclosure Statement should not be construed as a representation that a search has been made or that information cited is, or is considered to be, material to patentability as defined in §1.56(b), or that any cited document listed or attached is (or constitutes) prior art. Unless otherwise indicated, the date of publication indicated for an item is taken from the face of the item and Applicant(s) reserve(s) the right to prove that the date of publication is in fact different.

If any fees or charges are deemed required at this time in connection with the application, the same may be charged to our Patent and Trademark Office Deposit Account No. 03-2412.

Respectfully submitted,

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Dated: March 13, 2008